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Hideomi Suzawa et al.

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Examiner: Viktor Simkovic

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SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD TH

Commissioner for Patents Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Copies of the references listed on the attached form PTO-1449 are enclosed.

This filing is being made with the filing of a Request for Continued Examination. No fee is required.

Respectfully submitted,

Date: March 20, 2003

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